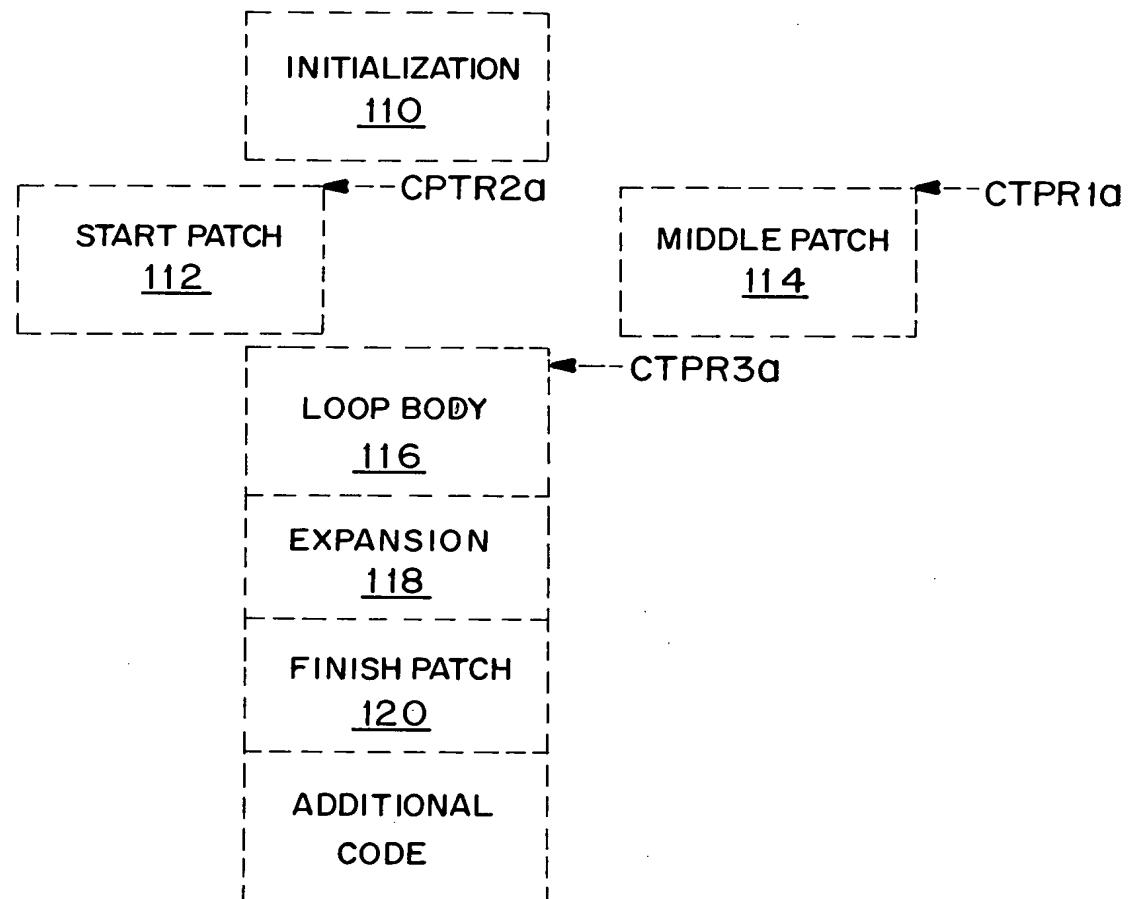


APPROVED BY	O.G. FIG.	
DRAFTSMAN	CLASS	SUBCLASS

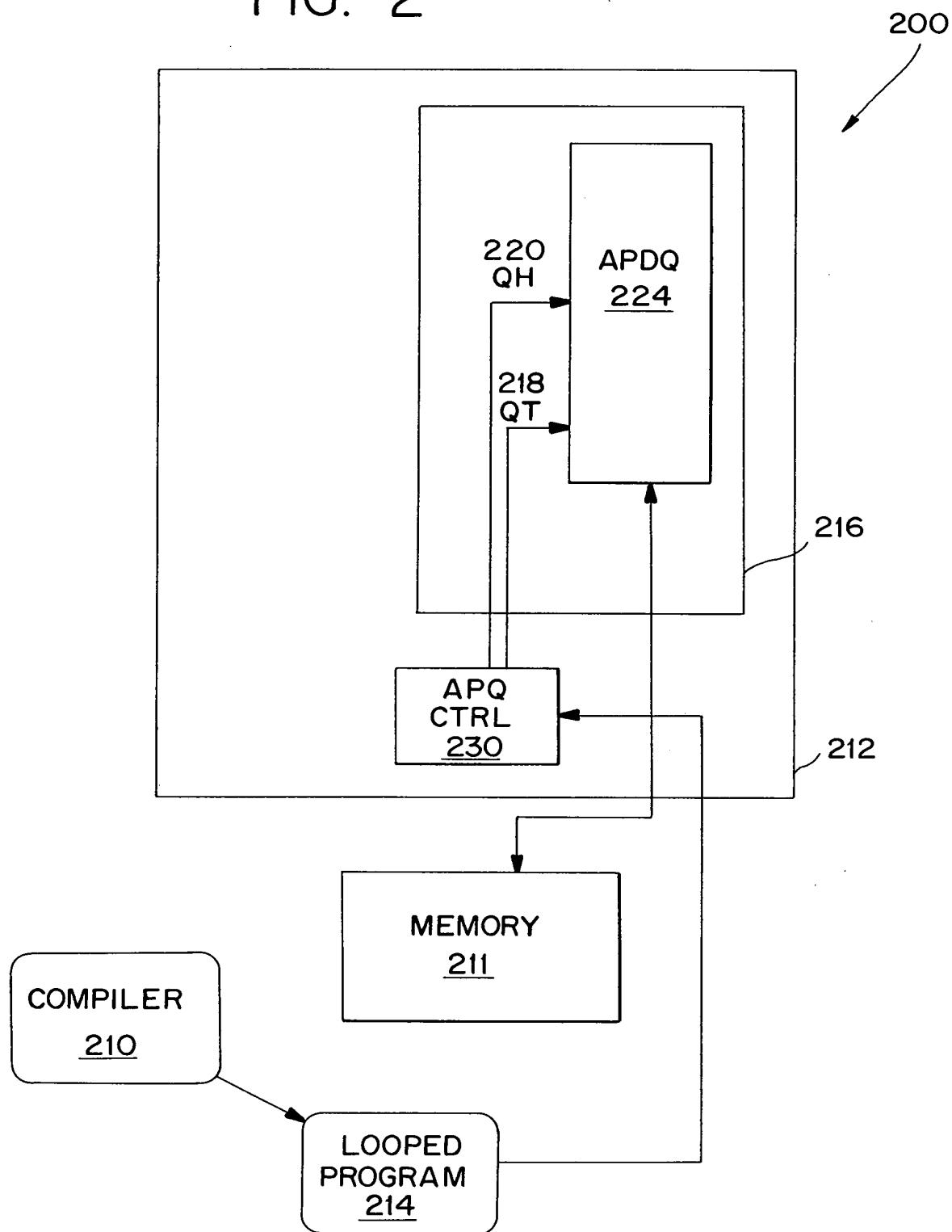
5889985

FIG. 1



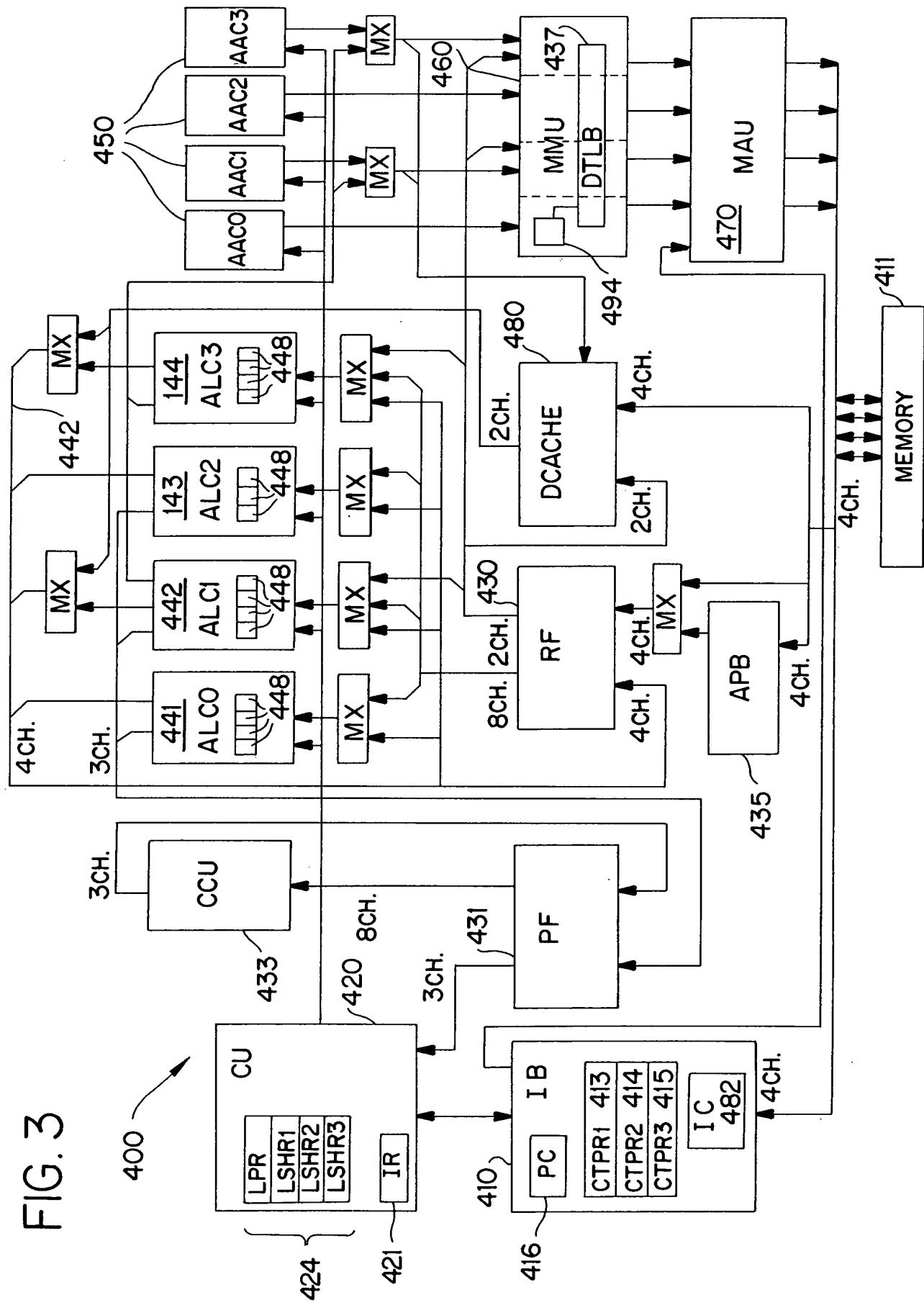
APPROVED	O.G. FIG:	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 2



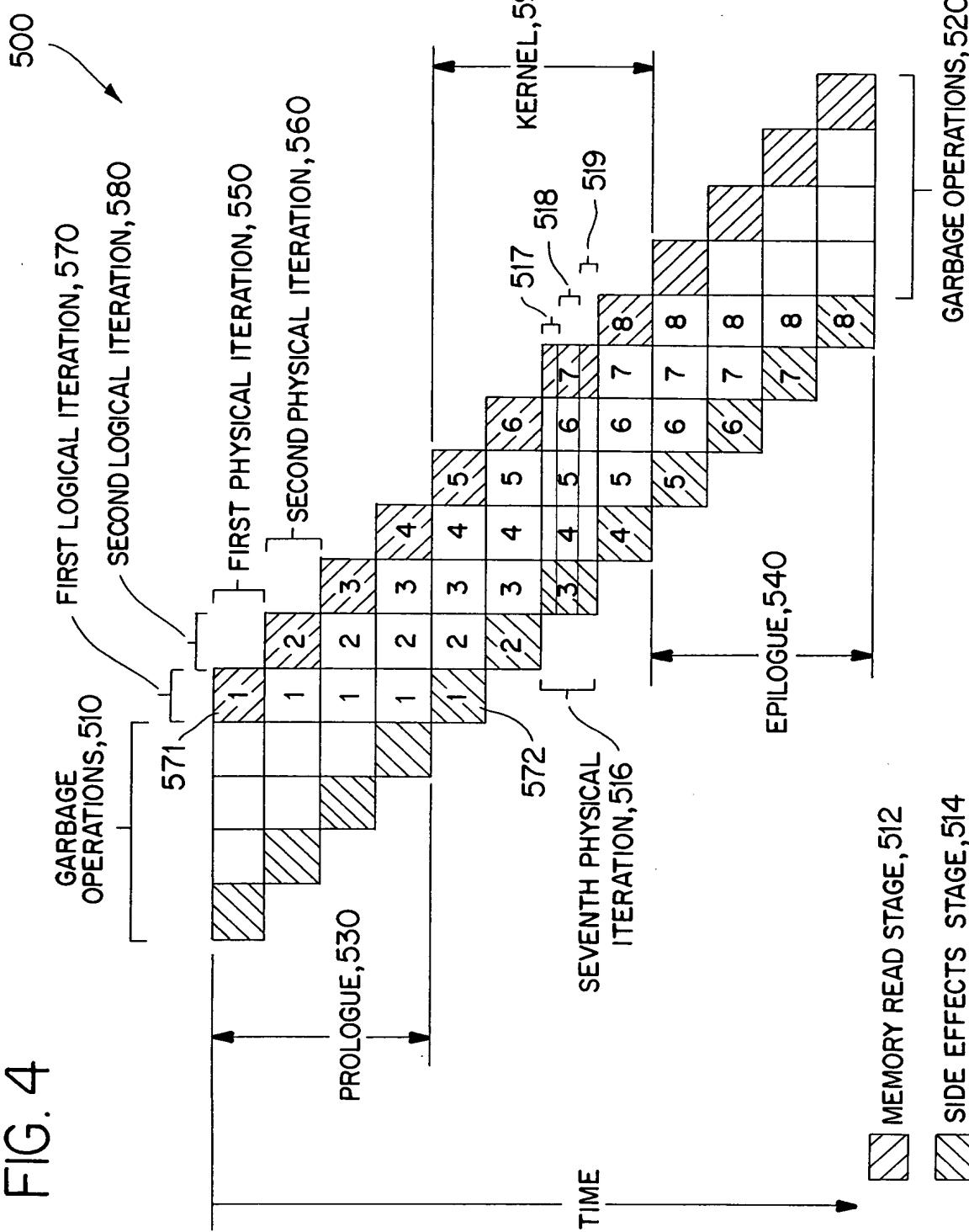
APPROVED BY DRAFTSMAN	O.G. FIG. CLASS    SUBCLASS
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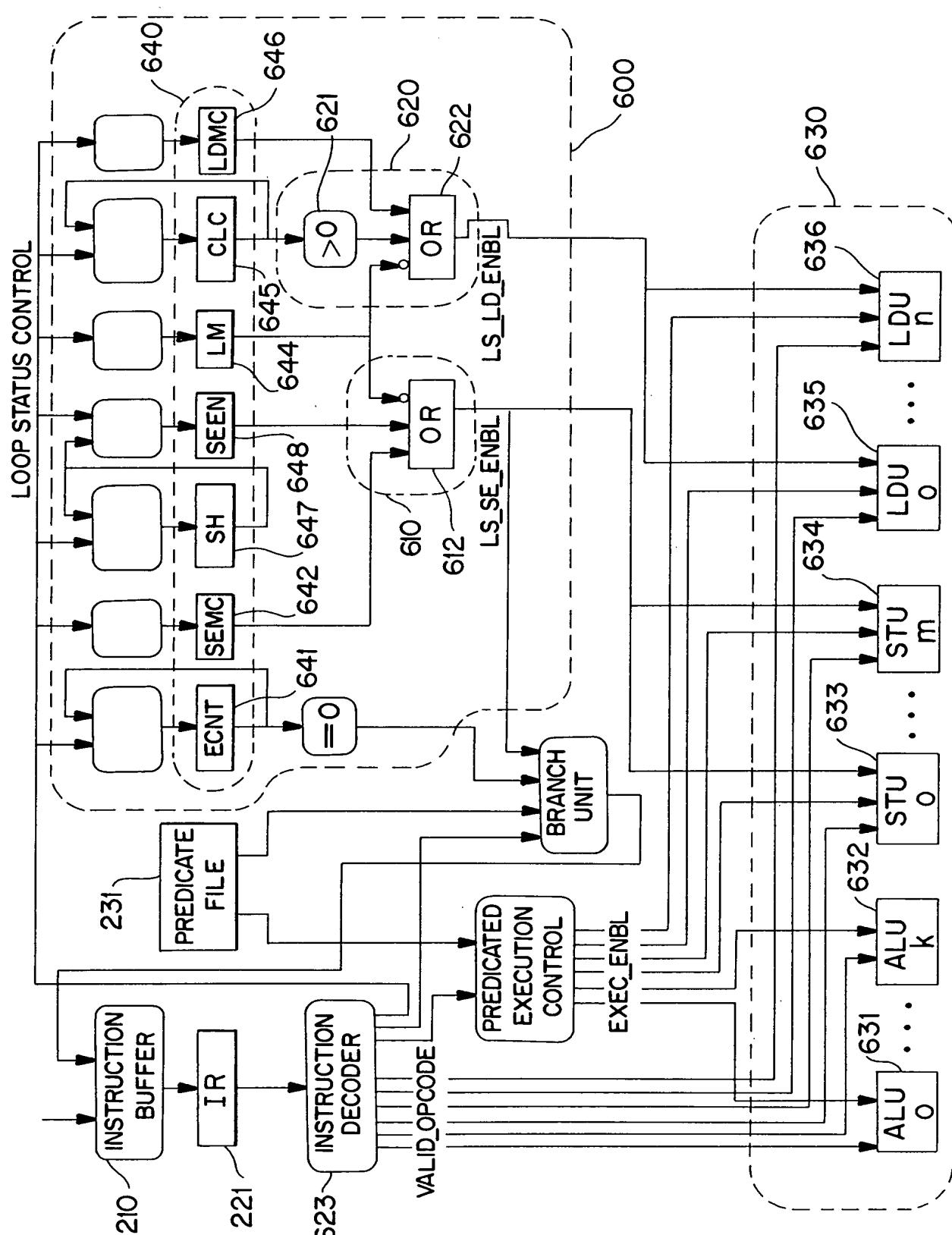
FIG. 3



APPROVED BY DRAFTSMAN	C.G. FIG. CLASS    SUBCLASS
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FIG. 4





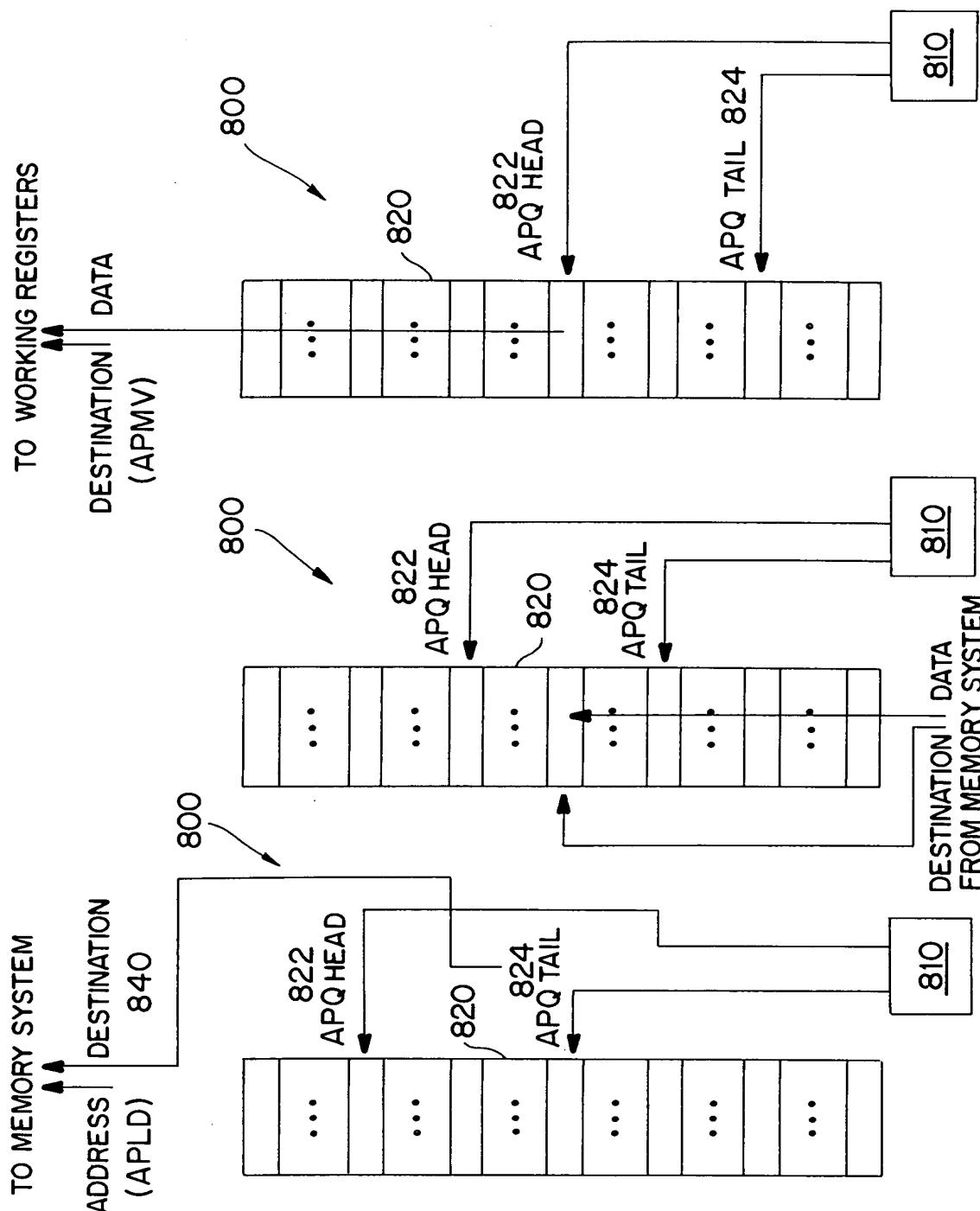
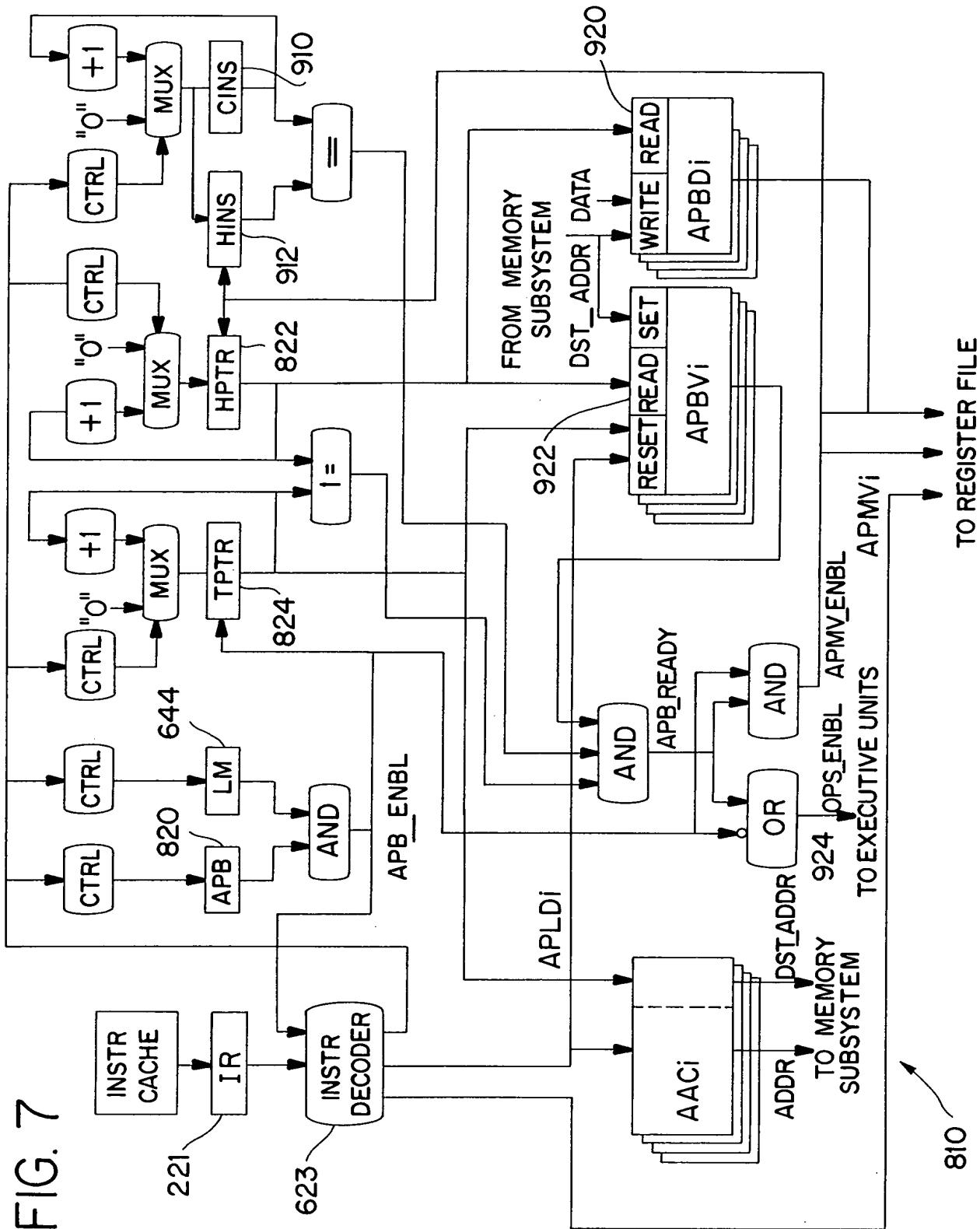


FIG. 6a

FIG. 6b

FIG. 6c



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

PI	clc	ecnt	cldovl	tptr	hptr	cins	hins	apb	ready
1	4	1	0	0	0	0	0	0	0
				1	0	1	0	0	0
				2	0	2	0	0	0
				3	0	3	0	0	0
				4	0	0	0	0	0
2	3	1	1	5	0	1	0	0	0
				6	0	2	0	0	0
				7	0	3	0	0	0
				8	0	0	0	1	1
3	2	1	2	9	1	1	1	1	1
				10	2	2	2	1	1
				11	3	3	3	1	1
				12	4	0	0	1	1
4	1	1	2	13	5	1	1	1	1
				14	6	2	2	1	1
				15	7	3	3	1	1
				16	8	0	0	1	1
5	0	1	2	17	9	1	1	0	0
				18	9	2	1	0	0
				19	9	3	1	0	0
				20	9	0	1	0	0
6	0	0	3	20	9	1	1	1	1
				20	10	2	2	1	1
				20	11	3	3	1	1
				20	12	0	0	1	1
7	0	0	2	20	13	1	1	1	1
				20	14	2	2	1	1
				20	15	3	3	1	1
				20	16	0	0	1	1
8	0	0	1	20	17	1	1	1	1
				20	18	2	2	1	1
				20	19	3	3	1	1

FIG. 8A

APPROVED BY DRAFTSMAN	O.G. FIG.
	CLASS      SUBCLASS

LOGICAL ITERATIONS				MEMORY DATA
0	1	2	3	
APLD <sub>a0</sub> >APB[0] APLD <sub>b0</sub> >APB[1] - -	APLD <sub>a1</sub> >APB[4] APLD <sub>b1</sub> >APB[5] - -	APLD <sub>a2</sub> >APB[8] APLD <sub>b2</sub> >APB[9] - -	APLD <sub>a3</sub> >APB[12] APLD <sub>b3</sub> >APB[13] - -	APB[0]= <sub>a0</sub> APB[1]= <sub>b0</sub> APB[4]= <sub>a1</sub> APB[5]= <sub>b1</sub> APB[8]= <sub>a2</sub> APB[12]= <sub>a3</sub> APB[9]= <sub>b2</sub> APB[13]= <sub>b3</sub>
APMV <sub>a0</sub> APMV <sub>b0</sub> FMUL -	APMV <sub>a1</sub> APMV <sub>b1</sub> FMUL -	APMV <sub>a2</sub> - - -	APMV <sub>a3</sub> APMV <sub>b3</sub> FMUL -	
STORECO	STOREC1	STORE C2	STORE C3	

FIG 8B